
Interactive Software Verification

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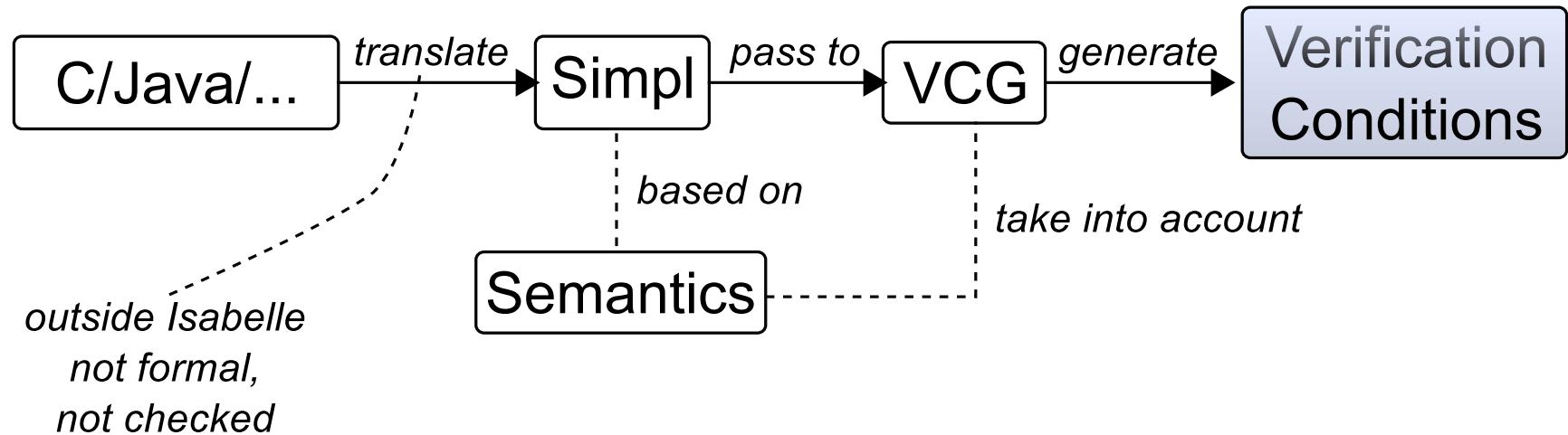
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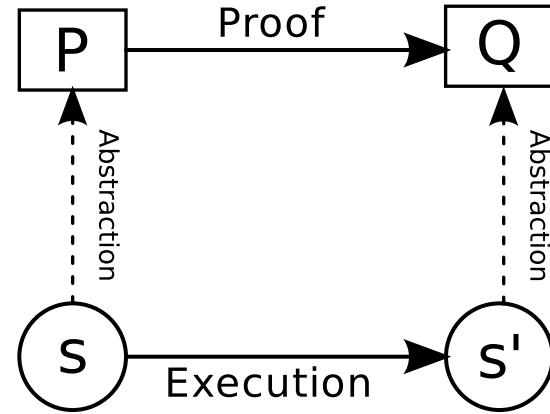
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- Shallow embedding of SimplC to Simpl
- Finally: start proving programs correct!

Recap

The General Plan





- Definition of correctness

$$\Gamma \models P \text{ c } Q, A \equiv \\ \forall \text{ st. } s \in \text{Normal} ' P \longrightarrow \\ \Gamma \vdash \langle c, s \rangle \Rightarrow t \longrightarrow \\ t \in \text{Normal} ' Q \cup \text{Abrupt} ' A$$

- Verification logic: $\Gamma, \Theta \vdash P \text{ c } Q, A$

- Soundness of VCG

$$\Gamma, \Theta \vdash P \text{ c } Q, A \implies \Gamma, \Theta \models P \text{ c } Q, A$$

Embedding SimplC into Simpl

- Phases of the embedding
 1. Parser yields: abstract syntax tree (AST)
 2. Type-checker: annotates with types
 3. Translator: creates Simpl commands (needs type info)
 - General considerations
 - Keeping track of “the current state”
 - Accessing / modifying the state
 - Translating primitive operations
 - Handling side-effects in expressions
 - Generating “safety checks”
 - Presentation: by example, using Simpl external syntax
-

- The assignment

j = i;

- Becomes

'j' := 'i'

- Internally \approx

(Basic($\lambda s.$ update-locals j_') (lookup-locals i_') s))

- Write to the local variable j
- The value obtained by reading local variable i
- (Naming convention post-fix '_')

- The assignment

j = i + 1;

- Becomes

'j ::= 'i + 1

- Internally (Groups.plus-class.plus is name of "+")

(Basic($\lambda s. update-locals j_1$)
(Groups.plus-class.plus(lookup-locals i_1 s) 1) s)

- For each primitive operator

- Evaluate the two operands into HOL values
- Then apply the corresponding HOL computation

- Wrap an array into a new datatype

datatype 'a array = Array "int × (int ⇒ 'a)"

- Basic: modify / read array

amap f a ≡ **case** a **of** Array a' ⇒ Array (f a')

aacc f a ≡ **case** a **of** Array a' ⇒ f a'

- Then define read/write access (functionally)

aget i a ≡ aacc (λ(n,c). c i) a

aset i x a ≡ amap (λ(n,c). (n, c(i := x))) a

- And a safety check

acheck i a ≡ aacc (λ(n,c). 0 ≤ i ∧ i < n) a

- Prove basic simplification rules (and declare `simp`)

$$\text{aget } i(\text{aset } i \times a) = x$$

$$i \neq j \implies \text{aget } i(\text{aset } j \times a) = \text{aget } i \ a$$

$$\text{acheck } i \ a = (0 \leq i \wedge i < \text{alength } a)$$

$$\text{alength } (\text{aset } i \times a) = \text{alength } a$$

- Interaction of `aget` and `aset` (two cases)
- Unfolding `acheck` such that the simplifier proves it
- Interaction of `alength` and `aset`

- The array access

```
j = a[i];
```

- Becomes

Guard "array-bounds" {acheck `i `a} (`j==aget `i `a)

- Evaluate acheck in the current state
 - ⇒ User must prove that check returns true
 - Then perform the modification (without further checks)

- The assignment

$i = a[i = i + 1];$

- Becomes

```
'i >> a_.
  ('i == a_ + 1;;
   Guard"array-bounds" {acheck(a_ + 1) 'a}
   ('i == aget(a_ + 1) 'a))
```

- Read \gg as “bind” (derived from DynCom, left out in overview)
 - Read i in the current state, keep the result as a_-
 - Update i to $a_- + 1$
 - Then check safety of access (Seq, written $; ;$)
 - Finally modify i

- The conditional

```
if (i > 0) r = 1;  
else r = 2;
```

- Becomes as expected (internal representation: Cond)

IF $0 < 'i$ THEN ' $r := 1$ ' ELSE ' $r := 2$ ' FI

- Combination with state updates is possible

```
if ((i = i - 1) > 0) r = 1;  
else r = 2;
```

- Becomes

$'i \gg a_-$
(' $i := a_- - 1$ '; IF $0 < a_- - 1$ THEN ' $r := 1$ ' ELSE ' $r := 2$ ' FI)

- Problem: side-effects in the while expression

```
while ( (i = i - 1) >= 0) j = j + 1;
```

- Simpl's While does not support this directly
- Trick: use While True and break by Throw

```
TRY WHILE True
  DO `i >> a_.
    (`i == a_- 1;;
     IF 0 ≤ a_- 1 THEN `j == `j + 1
     ELSE `thrown_ == ThrowBreak;; THROW FI)
    OD
  CATCH IF `thrown_ = ThrowBreak THEN SKIP ELSE THROW FI END
```

- Keep exception in local variable `thrown_`
- Catch only this kind of exception

```
while ( (i = i - 1) >= 0) j = j + 1;
```

- Unwind the execution of side-effects

```
'i >> a_.
('i == a_-1;;
 'test_ == 0 ≤ a_-1)
WHILE 'test_ DO
  'j == 'j + 1
  'i >> a_.
  ('i == a_-1;;
   'test_ == 0 ≤ a_-1)
OD
```

- Yields equivalent verification conditions
- (Aesthetic) Disadvantage: duplication of code / no 1-1 correspondence between code and embedding

- Given: source language SimplC
 - Standard compiler front-end
 - Parse using lex/yacc
 - Type-check
 - Define semantics by translation to Simpl
- ⇒ SimplC “inherits” semantics of Simpl
- Now forward: generate VCs for the Simpl program!

Using the VCG

A Basic Example

verify-statement swap

vars: {*

 int i;

 int j;

 int t;

*}

pre: "i = I ∧ j = J"

post: "i = J ∧ j = I"

{*

 t = i;

 i = j;

 j = t;

*}

apply vcg

apply simp

done

- Note free variables I and J

pre: "i=I ∧ j=J"

post: "i=J ∧ j=I"

⇒ logical variables (or auxiliary variables)

- Connect initial and final state
- ⇒ Specification as input/output-relation
- Goal: post-condition refers to result vars & logical vars
- Generated goal/triple for example

$$\begin{aligned} \forall J I. \Gamma \vdash \{ \& i=I \wedge \& j=J \} \\ \quad \& t := \& i; (\& i := \& j; \& j := \& t) \\ \{ \& i=J \wedge \& j=I \} \end{aligned}$$

- Goal: multiply i and j by summation

```
r = 0;  
while (i > 0) {  
    r = r + j;  
    i = i - 1;  
}
```

- The while rule was

$$\frac{\begin{array}{c} P \implies I \\ \{ I \wedge t \} b \{ I \} \\ I \wedge \neg t \implies Q \end{array}}{\{ P \} \text{while } (t) b \{ Q \}}$$

- We need to “guess” / “invent” a suitable invariant

- Generalize the desired post-condition
 - Loop test is $a \neq b$, then post-condition from $a = b \wedge I$
 - If loop test is $a < b$, then add $a \leq b$ to invariant
 - Describe (precisely) the achieved partial result
 - Consider the variables that are modified in the body
 - And capture the relationship between them
 - . . . and the relationship to the input values (logical vars)
 - Add safety assertions
 - About the range of index variables
 - About pointers not being null
 - Preserve information from before loop
-

```
r = 0;  
while (i > 0) {  
    r = r + j;  
    i = i - 1;  
}
```

- Changing variables r , i
 - Initial value of i is I
- ⇒ What are the relationships?
- ⇒ What is the current partial result?
- Look hard at the code, think a bit
 - See: j has been added a few times to r
 - More precisely: $I - i$ times!

$$r = (I - i) * j;$$

```
i = 0;  
while (i != n) {  
    dst[i] = src[i];  
    i = i + 1;  
}
```

- Post-condition: all elements have been copied
- Changing variables: i , src , dst
- Invariant: partial result is “copy up to i ”

$$\forall 0 \leq j < i. \text{dst}[j] = \text{src}[j]$$

- ⇒ With $i = n$ after end of loop matches post-condition
- Safety-condition: i is legal index in both arrays
- ⇒ Include into pre-condition
-

```
s = 0;  
i = 0;  
while (i != n) {  
    s = s + a[i];  
    i = i + 1;  
}
```

- Post-condition: $s = (\sum_{k=0..< N} a[k])$
- Inventing the invariant
 - Changing variables: i, s
 - Partial result: sum up to i (excluded) in s
$$s = (\sum_{k=0..< i} a[k])$$

- Side-conditions for proper array access

$$0 \leq i \wedge i \leq n \wedge n = \text{length } a$$

```
s=0; i=0;
/*@ 0 ≤ i ∧ i ≤ n ∧ n = alength a ∧ n = N ∧
   s = (∑ k=0..<i. aget k a) */
while (i != n) {
    s = s + a[i];
    i = i + 1;
}
apply vcg
apply auto
apply (simp add: ivl-split-last)
done
```

- Pre-condition: bind logical variables & safety

$$n = \text{alength } a \wedge n = N \wedge 0 \leq n$$

- Need one insight (by looking at the goals; lemma ivl-split-last)

$$i \leq j \implies \{(i::\text{int})..<j+1\} = \{i..<j\} \cup \{j\}$$

- We have gone the whole way
 - Define syntax & semantics of SimplC
 - Parse language SimplC
 - Translate SimplC to Simpl as shallow embedding
 - ⇒ Defines the semantics of SimplC
 - Generate verification conditions
 - ⇒ Program correctness by soundness of VCG
 - Embedding
 - Mostly straightforward
 - Keep track of “current” state
 - Side-effects in the while condition
 - Basics of verification: logical variables & loop invariants
-